



NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- · Built-In Biasing Resistors
- Surface Mount Package Suited for Automated Assembly
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

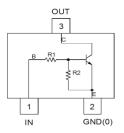
R1(NOM)	R2(NOM)
10kΩ	10kΩ

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



Top View



Device Schematic

Ordering Information (Notes 4 & 5)

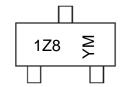
	Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
	ADTC114EUAQ-7	Automotive	1Z8	7	8	3,000
Г	ADTC114EUAQ-13	Automotive	1Z8	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

SOT323



1Z8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	202	20 20	21	2022	2023	2024	2025	2026
Code	D	Е	F	G	Н		I	J	K	L	М	Ν
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>	Vcc	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	V_{IN}	-10 to +40	V
Output Current	lo	50	mA
Output Current	I _C (Max)	100	mA

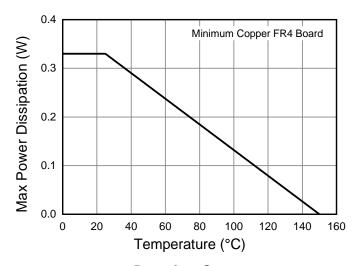
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_{D}	330	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ hetaJA}$	375	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

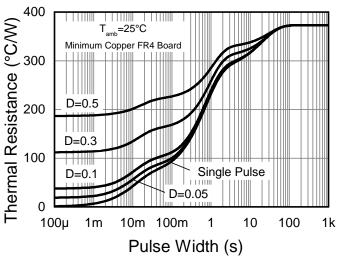
Note: 6. Mounted on FR4 PC Board with minimum recommended pad layout.



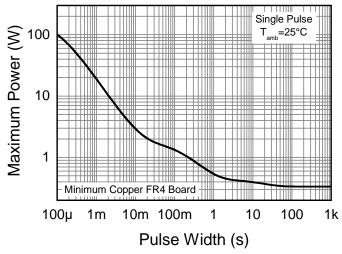
Thermal Characteristics and Derating Information



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation

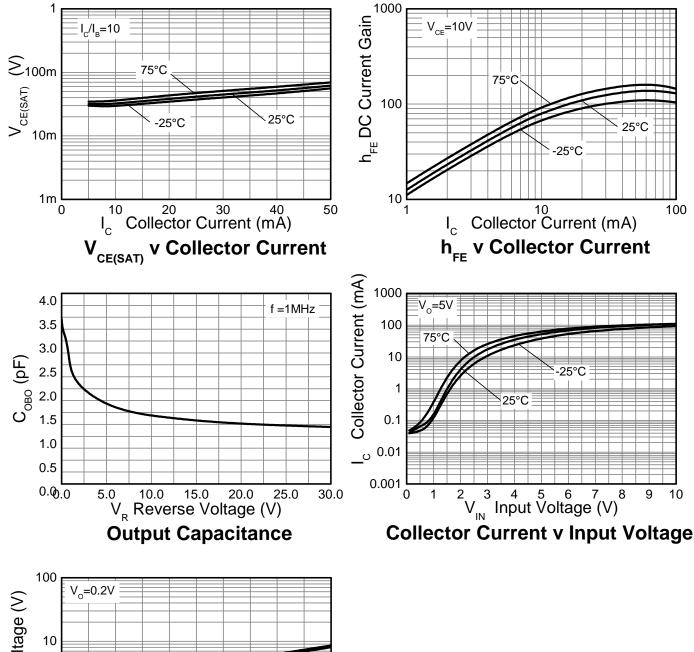


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{I(OFF)}	0.5	1.1		W	$V_{CC} = 5V, I_{O} = 100\mu A$
Imput voltage	V _{I(ON)}	_	1.9	3.0	V	$V_O = 0.3V, I_O = 10mA$
Output Voltage	V _{O(ON)}	_	0.1	0.3	V	$I_{O}/I_{I} = 10mA / 0.5mA$
Input Current	l _l	_	_	0.88	mA	$V_I = 5V$
Output Current	I _{O(OFF)}	_	_	0.5	μΑ	$V_{CC} = 50V, V_{I} = 0V$
DC Current Gain	Gı	30	_	_	_	$V_0 = 5V, I_0 = 5mA$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	_
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20	_	+20	%	_
Gain-Bandwidth Product	f⊤	_	250	_	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



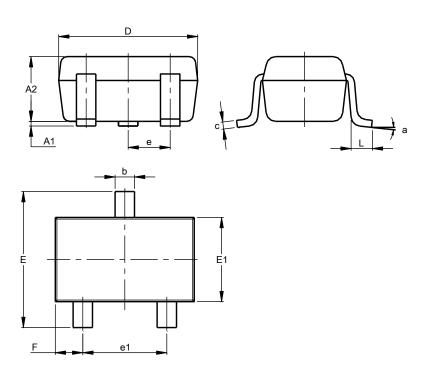
Input Voltage v Collector Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

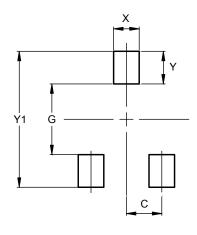


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
E	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	.650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	value
Difficusions	(in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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